



15MAT31

OR

- 8 a. Given f(40) = 184, f(50) = 204, f(60) = 226, f(70) = 250, f(80) = 276, f(90) = 304, find f(38) using Newton's forward interpolation formula. (06 Marks)
 - b. Use Lagrange's interpolation formula to fit a polynomial for the data :

С

	1	3	4	
y 0-12	0	6	12	
Hence estimate y at $x = 2$				(05 Marks)
Evaluate $\int \frac{1}{1+x^2} dx$ by Weddle's rule tak	king sev	en ordii	nates and	d hence find log _e 2.

(05 Marks)

Module-5

- 9 a. Find the area between the parabolas $y^2 = 4x$ and $x^2 = 4y$ using Green's theorem in a plane, (06 Marks)
 - b. Verify Stoke's theorem for the vector $\vec{F} = (x^2 + y^2)i 2xyj$ taken round the rectangle bounded by x = 0, x = a, y = 0, y = b.
 - c. Find the extremal of the functional : $\int_{-\infty}^{\infty} [y' + x^2(y')^2] dx$.

(05 Marks)

OR

- 10 a. Verify Green's theorem in a plane for $\oint_c (3x^2 8y^2) dx + (4y 6xy) dy$ where c is the boundary of the region enclosed by $y = \sqrt{x}$ and $y = x^2$. (06 Marks)
 - b. If $\overrightarrow{F} = 2xyi + yz^2j + xzk$ and S is the rectangular parallelopiped bounded by x = 0, y = 0, z = 0, x = 2, y = 1, z = 3 evaluate $\iiint \overrightarrow{F} \cdot \overrightarrow{n} \, ds$. (05 Marks)

c. Find the geodesics on a surface given that the arc length on the surface is $S = \int_{y_1}^{x_2} \sqrt{x[1+(y')^2]} dx.$ (05 Marks)



2. Any revealing of identification, appeal to evaluator and /or equations written eg. 42+8 = 50, will be treated as malpractice. Important Note : 1. On completing your answers, compulsorily draw diagonal cross lines on the remaining blank pages

OR

- a. Explain the small signal model of the FET. 4
 - b. Compare JFET and MOSFET.
 - c. Draw the JFET common drain configuration circuit. Drive Z_i , Z_0 and A_V using small signal (07 Marks) model.

Module-3

- The input power to a device is 10,000W at a voltage of 1000V. The output power is 500W 5 a. and the output impedance is 20Ω
 - i) Find a power gain in decibels
 - ii) Find the voltage gain in decibels
 - iii) Find input impedance.

(06 Marks)

(06 Marks)

- Describe Miller's effect and derive an equation for Miller input and output capacitance. b. (06 Marks)
- Discuss the effect of various capacitors on low-frequency response of BJT amplifier. (04 Marks)

OR

- An amplifier rated 40W output is connected to a 10Ω speaker.
 - i) Calculate the input power required for full power output if the power gain is 25dB.
- ii) Calculate the input voltage for rated output if the amplifier voltage gain 40dB. (04 Marks) Determine the high-cutoff frequencies of JFET amplifier for the following specification: b.

 $C_G = 0.01 \mu F$, $C_C = 0.5 \mu F$, $C_S = 2 \mu F$, (06 Marks)

Explain the effect of multistage frequency of an amplifier. C.

Module-4

- Mention the types of feedback connections. Draw their block diagrams indicating input and 7 a. output signal. (08 Marks)
 - With a neat circuit diagram, explain the working principle of FET phase-shift oscillator, b. with relevant equations. (08 Marks)

OR

- What are the effects of negative feedback in an amplifier? Show how bandwidth of an 8 a. amplifier increases with negative feedback. (06 Marks)
 - b. With a neat circuit and waveforms, explain the working operation of UJT relaxation oscillator. (05 Marks)
 - c. Determine the voltage gain, input and output impedance with feedback for voltage series feedback having A = -100, $R_i = 10 \text{ K}\Omega$ and $R_0 = 20 \text{ k}\Omega$ for feedback factor $\beta = -0.1$.

(05 Marks)

(03 Marks)

(06 Marks)

Module-5

- 9 a. With a neat circuit diagram, explain the operation of a series –fed class A power amplifier and prove that $\eta = 25\%$. (08 Marks)
 - b. Calculate the output voltage and the zener current in the regulator circuit of Fig.Q9(b) with $R_L = 1K\Omega$. (04 Marks)



c. Calculate the harmonic distortion components for an output signal with fundamental amplitude of 2.5V, second harmonic amplitude of 0.25, third harmonic amplitude of 0.1 V and fourth harmonic amplitude of 0.05V. Also find total harmonic distortion. (04 Marks)

OR

- 10 a. Explain the operation of a transformer coupled, push-pull class –B amplifier and derive its conversion efficiency. (07 Marks)
 - b. Explain the fold -back current limiting circuit of voltage series regulator. (05 Marks)
 - c. Determine the regulated voltage and currents of shunt regulation of Fig.Q10(C). (04 Marks)

		CBCS Scheme	
US	N		15EC33
		Third Semester B.E. Degree Examination, June/Jul	y 2018
		Digital Electronics	
Ti	ime:	3 hrs.	Max. Marks: 80
		Note: Answer any FIVE full questions, choosing ONE full question from each module.	
		Module-1	
1	a. b. c.	Given, $F = A(B + C) + D$, obtain : i) minimal SOP ii) minimal POS iv) canonical POS. Realize a circuit for Ex-NOR using only four NOR gates. Simplify the function using K-map. : $Y = f(a, b, c, d) = \sum_{m} (0,1,2,3,5,6,8,10,15)$.	iii) canonical SOP (08 Marks) (02 Marks)
		Write the simplified SOP expression.	(06 Marks)
2	a.	Simplify the following function using Quine – McClusky method :	
2		$P = f(a, b, c, d) = \Sigma_m(0, 2, 3, 5, 8, 10, 11, 13)$.	(06 Marks)
	9.	Reduce the following Boolean function using K-map and realize the si- using NOR gates.	implified expression
		$T = f(a, b, c, d) = \Sigma_m(0, 2, 3, 5, 6, 7, 8, 9) + \Sigma_d(10, 11, 12, 13, 14, 15).$	(06 Marks)
	C.	Prove that, $ABC + ABC + \overline{ABC} + \overline{ABC} = AB + BC + CA$	(04 Marks)
		Module-2	
3	a.	Design a binary full subtractor using logic gates. Write a truth table 1	Implement the logic
	h	circuit using basic gates.	(06 Marks)
	0.	suitable logic gates.	(10 Marks)
			, , ,
		OR	
4	a.	Implement full adder using 4 : 1 multiplexer (MUX).	(08 Marks)
	υ.	with a heat logic diagram, explain carry look ahead adder.	(08 Marks)
		Module-3	
5	a	Obtain the characteristic equation for D and T flin-flop	(04 Marks)
c	b.	Explain the working of a master-slave SR flip-flop with the help of	of a logic diagram,
		function table, logic symbol and timing diagram.	(08 Marks)
	C.	Differentiate sequential logic circuit and combinational logic circuit.	(04 Marks)
		OR	
6	a.	Explain the working of master slave JK flip-flops with functional table	and timing diagram.
	b.	Discuss the difference between a flin-flon and latch	(08 Marks)
	с.	Derive the characteristic equations of SR and JK flip-flops.	(04 Marks)
		1 of 2	

(04 Marks)

(06 Marks)

Module-4

7 a. Design a synchronous mod-5 counter using JK flip-flops and implement it. (08 Marks)
 b. Design synchronous mod-6 counter using D flip-flop to generate the count sequence, (0, 2, 3, 6, 5, 1, 0). (08 Marks)

OR

- 8 a. Design divide by 6 synchronous counter using T flip-flops. Write state table and reduce the expression using K-map.
 (06 Marks)
 - b. Compare synchronous and asynchronous counters.
 - c. Design mod-6 ripple counter using T flip-flops.

A.C.

Module-5

9 a. Design a Moore type sequence detector to detect a serial input sequence of 101. (08 Marks)
b. Design a synchronous counter using JK - flip-flops to count the sequence 0, 1, 2, 4, 5, 6, 0, 1, 2. Use state diagram and state table. (08 Marks)

OR

10 a. Explain the Mealy model and Moore model of a clocked synchronous sequential network.
b. Design a Mealy type sequence detector to detect a serial input sequence of 101. (08 Marks)



2 a. Using Mesh current analysis, find the current through 24Ω in the circuit shown in Fig.Q2(a). (08 Marks)



b. For the network of Fig.Q2(b) determine the node voltage by nodal analysis.

(08 Marks)



Module-2

3 a. State superposition theorem. In the circuit of Fig.Q3(a), use the superposition principle to determine the value of ix. (08 Marks)



b. Obtain the Thevenin and Norton equivalent circuits at terminals AB for the network shown in Fig.Q3(b). (08 Marks)





OR



(08 Marks)

6 a. In the network shown in Fig.6(a), the switch K is opened at t = 0. At $t = 0^+$, solve for the



b. Determine the Laplace transform of the periodic saw tooth waveform of Fig.Q6(b). Use gate function. (08 Marks)



Module-4

- 7 a. Derive for a resonant circuit, the resonant frequency $f_0 = \sqrt{f_1 f_2}$, where f_1 and f_2 are the two half power frequencies. (07 Marks)
 - b. Find the value of L for which the circuit shown in Fig.Q7(b) is resonant at a frequency of w = 5000 rad/sec. (06 Marks)



A series RLC circuit has $R = 10\Omega$, L = 0.01H and $c = 0.01\mu$ F and it is connected across c. 10mV supply. Calculate : i) f₀ ii) Q₀ iii) B.w. (03 Marks)

OR

- 8 a. A series RLC circuit has a resistance of 10Ω, an inductance of 0.3H and a capacitance of 100µF. The applied voltage is 230V. Find : i) Resonant frequency ii) Quality factor iii) Lower and upper cut off frequencies iv) Bandwidth v) Current at resonance vi) currents at f₁ and f₂ vii) voltage across inductance at resonance. (08 Marks)
 - b. Derive an expression for the resonant frequency of a parallel resonant circuit. Also show that the circuit is resonant at all frequencies if $R_L = R_C = \sqrt{\frac{L}{C}}$ where R_L = Resistance in the inductor branch, R_C = resistance in the capacitor branch. (08 Marks)

Third Semester B.E. Degree Examination, June/July 2018 Electronic Instrumentation

GBCS SCHEME

Time: 3 hrs.

USN

1

2

3

4

5

b.

Max. Marks: 80

Note: Answer any FIVE full questions, choosing one full question from each module.

Module-1

- a. Briefly explain Gross Errors, Absolute error and relative error with examples. (05 Marks)
- b. Draw the block diagram of a true RMS voltmeter and explain its operation. (05 Marks)
- c. Design a multirange ammeter using Aryton Shunt for the ranges 0 10 mA, 100 mA and 1 A, using a D'Arsonval movement having internal resistance of 1 K Ω and a full scale current of 100 μ A. (06 Marks)

OR

- a. Sketch and explain the operation of a multirange ammeter using Aryton shunt. (05 Marks)
 b. A resistor of 1 KΩ with an accuracy of ±5%, carries a current of 10 mA. The current is measured with an ammeter of 30 mA full scale with an accuracy of ±2% at full scale. Calculate the power dissipiation in the resistor and the accuracy of the power measurement.
- c. What is the loading effect of a voltmeter of low sensitivity? A voltage of 100 V dc is applied across a series combination of two resistors R1 and R2 each of 10 K Ω . A voltmeter of sensitivity 1 K Ω /V is used to measure the voltage across R2 in the range of 50 V. Calculate the voltmeter reading and percentage error of reading. (06 Marks)

Module-2

a. Describe with diagram the operation of a Ramp type DVM. What are its limitations?

(i) With the help of a block diagram, explain the operation of a digital time period measurement instrument.

(ii) The lowest range of a $4\frac{1}{2}$ digit DVM is 10 mV full scale. Determine its sensitivity.

(08 Marks)

OR

- a. Describe with diagram, the operation of a successive approximation type DVM. (08 Marks)b. (i) With the help of a block diagram, explain the operation of a digital capacitance meter.
 - (ii) What are the outstanding characteristics of a DVM? (08 Marks)

Module-3

- a. Draw the block diagram of a simple CRO and state the functions of each block. What is the advantage of using -ve HV supply in CRO? (08 Marks)
 - b. Explain with the help of a block diagram of a function generator, how it generates the different waveforms. (08 Marks)

(08 Marks)

- 6 a. (i) Describe the operation of a digital storage oscilloscope with the help of a block diagram.
 - (ii) The number of vertical and horizontal tangencies of a Lissajous figure are 2 and 6 respectively. What is the frequency of the signal connected to vertical plates, if horizontal plate signal frequency is 1 kHz.
 (08 Marks)
 - b. Sketch the block diagram of a square and pulse generator and describe how it generates the square waveform and pulses. (08 Marks)

Module-4

- 7 a. (i) Explain with diagram the working of a phase sensitive detector.(ii) What is the principle of working of a stroboscope?
 - b. Draw the circuit of a Wheatstone's bridge and explain how it can be used to measure an unknown resistance. (05 Marks)
 - c. If the two arms of a Wheatstone's Bridge are R₁ = 1 KΩ and R₂ = 10 KΩ. Find the range of the third arm resistance R₃ to be used to measure unknown resistance R4 of the range 1 KΩ to 100 KΩ, in the fourth arm.

OR

- 8 a. Define Q factor. With diagram, explain the operation of a Q meter to measure Q and inductance of a coil. (08 Marks)
 - b. Draw the diagram of a Maxwell's Bridge and obtain the equations to measure R_x , L_x and Q. (05 Marks)
 - c. A Maxwell's Bridge has components values at balance as $C_1 = 0.01 \ \mu\text{F}$, $R_1 = 470 \ \text{K}\Omega$, $R_2 = 5.1 \ \text{K}\Omega$, $R_3 = 100 \ \text{K}\Omega$. Find the value of the inductive impedance connected in the fourth arm (Rx and Lx). (03 Marks)

Module-5

9 a. Explain the operation of a resistive position transducer.

AND BASSIN

(05 Marks)

b. Describe with diagram the operation of a piezo electric transducer. (05 Marks)
 c. With circuit diagram, explain the operation of a LVDT the method of measuring displacement. (06 Marks)

OR

- 10 a. (i) Explain with diagram the construction of a Bonded Resistance wire gauge. How does it senses strain/stress?
 - (ii) How it is used in a bridge arrangement with a dummy gauge and what is the advantages of such an arrangement? (08 Marks)
 - b. Briefly explain the construction and operation of a photoconductive cell and a photo transistor. (04 Marks)
 - c. With a circuit explain how a photo transistor can be used to operate a street light relay.

(04 Marks)



OR

- Solve Laplace's equation to determine the capacitance of a coaxial cable when the inner 6 a. (08 Marks) radius is 'a' and outer radius is 'b' respectively. (04 Marks)
 - State and explain 'stokes theorem'. b.
 - c. Given the vector magnetic potential $\vec{A} = x^2 \hat{a}_y + 2yz \hat{a}_y + (-x^2)\hat{a}_z$. Find magnetic flux (04 Marks) density.

Module-4

- Derive Lorentz force equation and mention the application of solution. (05 Marks) 7 2. b. A point charge Q = -1.2C has velocity $\vec{V} = (5\hat{a}_x + 2\hat{a}_y - 3\hat{a}_z)$ m/s. Find the magnitude of force exerted on the charge if,
 - $\vec{E} = -18 \hat{a}_x + 5 \hat{a}_y 10 \hat{a}_z \quad V/m$ i)
 - $\vec{B} = -4 \hat{a}_x + 4 \hat{a}_y + 3 \hat{a}_z$ T ii)
 - Both are present simultaneously. iii)

C. Briefly explain force between differential current elements. (06 Marks) (05 Marks)

OR

Discuss the magnetic boundary condition at the interface between two different magnetic 8 a. (05 Marks) materials.

Briefly explain potential energy and forces on magnetic materials. (05 Marks) b. A rectangular loop of wire in free space joins A(1, 0, 1), B (3, 0, 1) to C(3, 0, 4) to D (1, 0, 4) to A. The wire carries a current of 6mA flowing in â, direction from B to C. A filamentary current of 15A flows along the entire z-axis in the \hat{a}_z direction as shown in ii) Force on side AB iii) Total force on loop. Fig.Q.8(c). Find: i) Force on side BC

H

1 = 15 A

 $i = 6m \pi$

(06 Marks)

Fig.Q.8(c)

Module-5

- State and explain Faraday's law in point and integral form. (06 Marks) 9 a.
 - Derive Ampere's circuit law in point form and integral form suitable for Time-varying b. (07 Marks) fields.
 - c. Find the angular frequency at which the conduction current and displacement current are equal in medium with $\sigma = 5.6 \times 10^{-6}$ S/m and $\epsilon_r = 40$. (03 Marks)

OR

- 10 a. State and prove Poynting theorem.
 - b. Briefly explain skin depth and skin effect.
 - A 300MHz uniform plane wave propagation through fresh water for which $\sigma = 0$, $\mu_r = 1$ and $\epsilon_r = 78$. Calculate:
 - i) Attenuation constant
 - Phase constant ii)
 - iii) Wave length
 - Intrinsic impedance. iv)?

(05 Marks)

2 of 2

(06 Marks)

(05 Marks)





